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PATENT
TESSERA 3.0-078 DIV

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Applicant of
Joseph Fjelstad

Group Art Unit: 2811

Application Serial No. 09/020,647

Examiner: Unassigned

Filed: February 9, 1998

Date: July 21, 2000

For: **METHOD OF MAKING A
SEMICONDUCTOR CHIP PACKAGE**

X

Assistant Commissioner for Patents
Washington, D.C. 20231

LETTER TO THE OFFICIAL DRAFTSMAN

Sir:

Subject to the approval of the Examiner handling the above-identified application, please amend the drawings currently on file as indicated in the red-marked copy of Figure 2.

Please substitute the enclosed drawing sheet which incorporates the requested changes for the drawing sheet currently on file. After the Examiner has approved the requested amendment to Figure 2, Applicant will submit a new set of formal drawings incorporating the authorized changes.

If there are any additional charges in connection with this requested amendment, the Examiner is authorized to charge applicant's Deposit Account No. 12-1095 therefor.

Respectfully submitted,

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class mail in an envelope addressed to Assistant Commissioner for Patents, Washington, D.C. 20231 on July 21, 2000.

(Signature)

MICHAEL J. DOHERTY

(Typed or Printed Name of Person Signing Certificate)

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